

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7	((("5404563") or ("5555414") or ("5659786") or ("6026448") or ("6247109") or ("20030182353") or ("20030236972")).PN.	US-PGPUB; USPAT	OR	OFF	2008/01/04 12:23
S2	12	((("6339802") or ("6339801") or ("6854021") or ("5253344") or ("5564040") or ("5923890") or ("6996638") or ("7290070") or ("5345590") or ("5764899") or ("6085217") or ("6907605")).PN.	US-PGPUB; USPAT	OR	OFF	2008/01/04 12:28
S3	1	"718".clas. and (logical or virtual) adj (partition or processor) same distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:30
S4	5	"709".clas. and (logical or virtual) adj (partition or processor) same distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:30
S5	1	"712".clas. and (logical or virtual) adj (partition or processor) same distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:30
S6	51	"709".clas. and (logical or virtual) adj (partition or processor) and distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:40
S7	22	"718".clas. and (logical or virtual) adj (partition or processor) and distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:40
S8	10	"712".clas. and (logical or virtual) adj (partition or processor) and distributed adj processing	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:43

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S9	8	"5758157".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:45
S10	15	"5784617".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:48
S11	1	"7010602".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:48
S12	37	"718".clas. and logical adj partition same queue	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:48
S13	88	"718".clas. and logical adj partition and queue same (task or process or program or thread)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:48
S14	48507	"718".clas. and logical adj partition and (assign\$ or allocat\$) queue same (task or process or program or thread)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:49
S15	25	"718".clas. and logical adj partition and (assign\$ or allocat\$) near2 queue same (task or process or program or thread)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:55
S16	38	"709".clas. and logical adj partition and (assign\$ or allocat\$) near2 queue same (task or process or program or thread)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:56

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S17	5	"712".clas. and logical adj partition and (assign\$ or allocat\$) near2 queue same (task or process or program or thread)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/04 12:56
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[Bounds and approximations for self-initiating distributed simulation without lookahead - all 6 versions »](#)

RE Felderman, L Kleinrock - ACM Transactions on Modeling and Computer Simulation (TOMACS ... , 1991 - portal.acm.org

... and Phrases: Bounds, discrete-event ,simulation, **distributed processing**, ... **logical processor** does not cause reads or writes ... must maintain a **queue** of messages that ...

Cited by 23 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Modeling the Performance of Organizational Structures - all 3 versions »](#)

TW Malone, SA Smith - Operations Research, 1988 - JSTOR

... one of the functional departments-the arithmetic and **logical processor**. ... Classes under Preemptive Resume in M/M/m **Queues**. ... **Distributed Processing**: An Assessment. ...

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[Towards efficient multi-level threading of H. 264 encoder on Intel hyper-threading architectures - all 4 versions »](#)

YK Chen, X Tian, S Ge, M Girkar - ... and **Distributed Processing** Symposium, 2004. Proceedings. ... , 2004 - ieeexplore.ieee.org

... history array Duplicated Per **logical processor** architecture state ... **queue**, general instruction **queue** Table 2 ... Parallel and **Distributed Processing** Symposium (IPDPS ...

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... **processor** are handled only by that **logical processor**. ... of the International Parallel and **Distributed Processing** Symposium (IPDPS ... m=1...N, and m ≠ K) Work **queue** ...

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[Multiprocessing system including gating of host I/O and external enablement to guest enablement at ... - all 3 versions »](#)

RE Hough, RE Murray - US Patent 5,555,414, 1996 - Google Patents

... Abstract, "Guest layering **Distributed Processing** Support on Local Operating ... likely to reassign a **logical processor** from a ... on a 65 guest software **queue** for the ...

Cited by 10 - [Related Articles](#) - [Web Search](#)

[\[PDF\] DISTRIBUTED C+ - all 2 versions »](#)

H Carr - 1994 - haroldcarr.net

... DC++ is designed to run on the Mayy **Distributed Processing** System. ... 4.5 Delay **Queue** Operations : : : : 94 ...

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[\[PDF\] Tradeoffs in designing organizations: implications for new forms of](#)

human organizations and computer ... - all 3 versions »

TW Malone, SA Smith - 1984 - dspace.mit.edu

... one of the functional departments--the arithmetic and **logical processor**.

In many time-sharing ... scheduling jobs in the printer **queue**). ...

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An implementation of nCUBE C execution environment under Mach - all 3 versions »

H Midorikawa, S Uchiyama, H Iizuka - Communications, Computers, and Signal Processing, 1995. ..., 1995 - ieeexplore.ieee.org

... inserting a message to the **queue** due to ... The whoami() also supplies a **logical processor**

node id ... Mach was designed to integrate **distributed processing** by multiple ...

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Parallelization of Bayesian network based SNPs pattern analysis and performance characterization on ... - all 4 versions »

JL Song, EWH Ge, SCLYZ Xuegong, ZWCW Zheng - Parallel and Distributed Systems, 2004. ICPADS 2004. ..., 2004 - ieeexplore.ieee.org

... L2 cache, global history array Duplicated Per **logical processor** architecture state ... history buffer Partitioned Re-order buffer, uop **queue**, memory instruction ...


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[PDF] in Optimistic PDES - all 6 versions »

F SARKAR, SK DAS - VLSI DESIGN, 1999 - hindawi.com

... parallel simulation [19], every lp proceeds with its simulation (ie, receives and processes the event messages from its own input **queue**) disregarding the ...

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[Distributed Quadtree Processing - all 7 versions »](#)

CH Chien, T Kanade - Proceedings of the first symposium on Design and ..., 1990 - Springer
... is one of the most crucial issues in **distributed processing**. ... put into a task **queue**,
and subsequently distributed to the **logical processor** which has ...

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[System and method for distributed processing in a node environment](#)

CA Jardin - 2004 - freepatentsonline.com

... table storage in the **logical processor** storage areas ... for example, systems having
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[Object oriented processor arrays - all 5 versions »](#)

JL Robinson - US Patent 6,567,837, 2003 - Google Patents

... No. 08/525, 948, filed Sep. 8, 1995 for **"Distributed Processing Systems Having A
Host Processor And At Least One Object Oriented Processor"**, now US Pat. No. ...

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X Tian, Y Chen, M Girkar, S Ge, R Lienhart, S Shah - 2003 - cecs.uci.edu

... the International Parallel and **Distributed Processing Symposium IPDPS** ... **processor**
are

handled only by that **logical processor**. ... from the taskq **queue** and executing it ...

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[Proteus: A reconfigurable computational network for computer vision - all 6 versions »](#)

RM Haralick, AK Somani, C Wittenbrink, R Johnson, ... - Machine Vision and Applications, 1995 - Springer

... If all is well, the loader performs the mapping of the **logical processor** names in
Proteus to the symbolic processor names used by the trans- lator to define ...

[Cited by 6](#) - [Related Articles](#) - [Web Search](#)

MSSM—A design aid for multi-stage systolic mapping - all 2 versions »

YT Hwang, YH Hu - The Journal of VLSI Signal Processing, 1992 - Springer

... (3) The processor array obtained from each stage's mapping will be called a **logical processor** array to differentiate it from the physical processor array ...

[Cited by 5](#) - [Related Articles](#) - [Web Search](#)

Compiler and runtime support for running OpenMP programs on Pentium-and Itanium-architectures - all 7 versions »

X Tian, M Girkar, S Shah, D Armstrong, E Su, P ... - High-Level Parallel Programming Models and Supportive ..., 2003 - ieeexplore.ieee.org

... in conjunction with the International Parallel and **Distributed Processing** Symposium IPDPS2003 ... participate in dequeuing tasks from the taskq **queue** and executing ...

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Slicing Analysis and Indirect Accesses to Distributed Arrays - all 3 versions »

R Das, J Saltz, R von Hanxleden - Languages and Compilers for Parallel Computing: 6th ..., 1994 - books.google.com

Page 162. Slicing Analysis and Indirect Accesses to Distributed Arrays Raja

Das 1 and Joel Saltz 1 and Reinhard von Hanxleden 1 Department ...

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[System for enterprise-wide work flow automation - all 5 versions »](#)

LK Poindexter, GR Seidman, SR Timm, BA Waddington - US Patent 6,338,074, 2002 - Google Patents

... and data that control the flow of information through the underlying **distributed processing** system ... is an instance of a work performer class; a work **queue** is an ...

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[InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable ... - all 6 versions »](#)

TM Pinkston, AF Benner, M Krause, IM Robinson, T ... - Cluster Computing, 2003 - Springer ... conjunction with the International Parallel and **Distributed Processing** Symposium (IPDPS ... be uniquely assigned to a **logical partition**, and the **queue** pairs can ...

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[Virtualization of a global interrupt **queue** - all 2 versions »](#)

WJ Armstrong, DA Larson, N Nayar - 2004 - freepatentsonline.com

... Workshop on Parallel and **Distributed Processing**, pp. ... processors associated with the **logical partition**; a virtual global interrupt **queue** configured to ...

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[Distributed Systems Design for Tactical Applications](#)

L Anderson, B Manning, L Slechta, T Wolff - OCEANS, 1979 - ieeexplore.ieee.org

... is partitioned into separate processing components; ie, a **distributed processing** system ... Three factors constitute an adequate description of a **logical partition**. ...

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[Partitioning For Virtual Machine Efficiency](#)

LD Anderson, BW Manning, LJ Slechta, TO Wolff - Compcon Fall 79. Proceedings, 1979 - ieeexplore.ieee.org

... partitioned into separate processing components; ie, a **distributed processing** System ... Three factors constitute an adequate description of a **logical partition**. ...

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[Collection schemes for distributed garbage - all 7 versions »](#)

SE Abdullahi, EE Miranda, GA Ringwood - Proceedings of the International Workshop on Memory ..., 1992 - Springer

... tables. A cluster is a **logical partition** of cells (a passive node) in contrast to the natural physical partition (of active nodes). ...

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[Different FDT's Confronted with Different ODP-Viewpoints of the Trader - all 4 versions »](#)

J Fischer, A Prinz, A Vogel - FME, 1993 - Springer

... The Reference Model of Open **Distributed Processing** (ODP RM ... tend to realize, that it is a **queue**. ... to group resources from a **logical partition** use: information to ...

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book Predictive dynamic load balancing of parallel and distributed rule and query processing - all 5 versions »

HM Dewan, SJ Stolfo, M Hernández, JJ Hwang - 1994 - ACM Press New York, NY, USA.
... inference cycle. However, under a synchronous processing policy, an efficient **distributed processing** ensemble requires that ...

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[Guided navigation in task-oriented 3D graph visualizations - all 9 versions »](#)

G Reina, S Lange-Last, K Engel - Theory and Practice of Computer Graphics, 2003.
Proceedings, 2003 - [ieeexplore.ieee.org](#)

... LPAR: An LPAR is a **logical partition** running on ... maximum number of iterations the **queue** can undergo ... and DFG SPP 1041 **"Distributed Processing** and Transmission ...

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U den Linden - FME'93: Industrial-strength Formal Methods: First ..., 1993 - [books.google.com](#)

... The Reference Model of Open **Distributed Processing** (ODP RM ... tend to realize, that it is a **queue**. ... to group resources from a **logical partition** use: information to ...

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SM Fry, HO Hempy, BE Kittinger - US Patent 4,403,286, 1983 - Google Patents
... pp. 1523-1525, Fen- nell, Jr., "**Queue** System For a Two Program, Pipelined
Instruction Unit". IBM Technical Disclosure, 8/77, pp. ...
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K Chipman, P Holzworth, J Loop, N Ransom, D Spears ... - Selected Areas in
Communications, IEEE Journal on, 1992 - [ieeexplore.ieee.org](#)
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[A real-time software architecture for robotics and automation - all 3 versions »](#)

JM Roberts, PI Corke, RJ Kirkham, F Pennerath, GJ ... - Robotics and Automation, 1999.
Proceedings. 1999 IEEE ..., 1999 - [ieeexplore.ieee.org](#)
... of this approach over a single 'monolithic' process are: **Logical partition** of tasks. ...
Distributed processing. ... FC = function call, MQ Posix message **queue**. ...
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[Data-parallel concurrent constraint programming - all 4 versions »](#)

BM Tong, HF Leung - The Journal of Logic Programming, 1998 - Elsevier
... **logical partition**, or simply a partition, is mapped to a number of identical
physical partitions. ... execute exactly the same initial **logical partition**. ...
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J Home - IBM Journal of Research and Development, 2001 - [research.ibm.com](#)
IBM Journal of Research and Development issue 45-5, Advanced
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C Cern, H Jin - Parallel I/O for Cluster Computing, 2003 - [books.google.com](#)
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Cern Université de Picardie Jules Verne, LaRIA, 5 rue du ...
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DA Burgoon, MD Davis, KE Dorow, TA Hitt, DD Mooney ... - 2004 - [freepatentsonline.com](#)
... continually rebuilt by defining logical partitions of tables of said data source,
ordering said logical partitions, and, for each **logical partition**: loading a ...
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[CITATION] JACK L. ROSENFELD, EDITOR

JE THORNTON, DJ MCBRIDE, EE SWARTZLANDER JR, GC ... - Fundamentals Handbook of Electrical and Computer Engineering, 1982 - John Wiley & Sons Inc

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B Jacob, International Technical Support ... - 2002 - redbooks.ibm.com

... pSeries server provides enhanced capability to execute Linux applications using the Linux operating environment in native mode or in a **logical partition** (LPAR ...

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[A Lightweight Framework for Executing Task Parallelism on Top of MPI - all 3 versions »](#)

RE Hadjidoukas - Recent Advances in Parallel Virtual Machine and Message ..., 2004 - [books.google.com](#)

... The stealing of a descriptor from a remote **queue** includes the corresponding data ...

When a **idle virtual processor** tries to find available work on remote nodes, it ...

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[\[PDF\] μSystem Annotated Reference Manual - all 9 versions »](#)

PA Buhr, HI Macdonald - 1991 - [cs.uwaterloo.ca](#)

... context switching. A cluster uses a single-**queue** multi-server queueing model for scheduling its tasks on its virtual processors. This ...

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YK Chen, X Tian, S Ge, M Girkar - Parallel and Distributed Processing Symposium, 2004. ..., 2004 - [ieeexplore.ieee.org](#)

... the return stack buffer are replicated for each **logical processor**. ... taskq pragma causes an empty **queue** to be ... block, the code inside the **task block** is enqueued ...

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X Tian, Y Chen, M Girkar, S Ge, R Lienhart, S Shah - 2003 - [cecs.uci.edu](#)

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[T LeBlanc](#)

[I Leslie](#)

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[E Hyden](#)

VERA: an extensible router architecture - all 18 versions »

S Karlin, L Peterson - Computer Networks, 2002 - Elsevier

... The relevance is that each **virtual processor** is its own scheduling domain with a ...
allocHWqueue(dir, depth): This function allocates a distributed **queue**, q, from ...

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A configurable multimedia middleware platform - all 11 versions »

G Coulson - Multimedia, IEEE, 1999 - ieeexplore.ieee.org

... s (ISO) Reference Model for Open **Distributed Processing**, the OMG has not ... The
asc_init()

command creates the run **queue** and **virtual-processor** resources when ...

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[ps] On the Physical Design of PRAMs* - all 11 versions »

F Abolhassan, R Drefenstedt, J Keller, WJ Paul, D ... - The Computer Journal, 1993 -
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... processors. Each **virtual processor** has its own register set in hardware. ... When the
sorted sequence leaves the array, the tags are stored in a FIFO **queue**. ...

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Multi-model parallel programming in psyche - all 8 versions »

ML Scott, TJ LeBlanc, BD Marsh - Proceedings of the second ACM SIGPLAN symposium on
..., 1990 - portal.acm.org

... ferent ways-as lightweight threads of various kinds, or requests on the **queue** of
a ... Because of time-slicing, the progress of a given **virtual processor** may be ...

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GS Blair, GC Fabio Costa, F Delpiano - Meta-level Architectures and Reflection: Second ...,
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... of such models in open **distributed processing** [Blair97 ... maps them on to the
underlying

virtual processor (s). In ... jobs, the scheduler must maintain a job **queue**. ...

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... GVT is the minimum of all **virtual processor** times (in this case, the virtual time of ... message with a lower timestamp will be waiting in the input **queue**, while a ...

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... On iWarp at most 20 communication **queues** are available on each node. ... A completely general **virtual processor** model would probably not be efficient enough, but a ...

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... NET 101 manages the execution of the Function by finding an available **Virtual Processor** with the capability of per -forming the ... This **distributed processing** 10 ...

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as a **virtual processor** sharing the ... The **queue** is scanned by one or more ...

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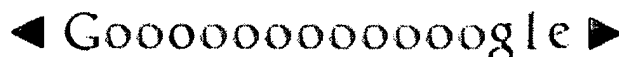
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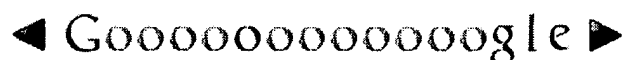
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... for example, systems having disjoint, **distributed processing** of tasks ... central processing

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... on an abstraction called a shared directory of unordered **queues**, which has ... the JavaNOW
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... user-level threads are created to execute tasks from a shared task **queue**. ... HPF allows the specification of **virtual processor** grids in the declaration section of ...

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... 65,000 **virtual processor** elements, over 1,000,000 connections, 0.45 MCPS; Mark IV: 236,000 ... equipped with a control logic block and an input activation **queue**. ...

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... An alternative is the use of a central parallel task **queue** that can be ... way, ie, after having executed a machine instruction of a **virtual processor** i, a ...

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... One can view the shared memory of the QSM model as a collection of **queues**, one per shared-memory location; requests to read or write a location **queue** up and ...

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... per physical processor is chosen such that, with respect to the cycle time of a **virtual processor**, the latency of ... 4.4 Parallel single-modify **queue** algorithm ...

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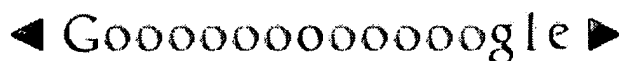
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... processors nish the work descriptor execution, the master **virtual processor** resumes the ... executed on the available processors through their local ready **queues**. ...

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... for example, systems having disjoint, **distributed processing** of tasks ... central processing

unit can be a **virtual processor**. ... Tstores have an inbound **queue**, or an ...

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... uler (single run-**queue** and the inverse of CPU usage as priority) is ... This thread, called a **virtual processor**, alternates between running one of two kinds of user ...

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... add the handling of data redistribution and the choice of the **virtual processor** grid where ... For instance, a server is a **queue** where tasks are stored when they ...

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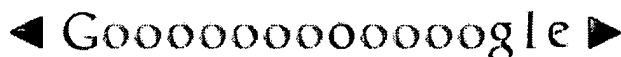
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... From the system's perspective, a worker represents a **virtual processor** with memory. ... for the agent to run on, and informs the agent about its message **queue**. ...

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... In network-level parallelism or **distributed processing** the execution of a computational task is divided to computers connected with a network. ...

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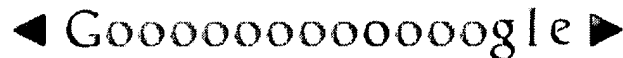
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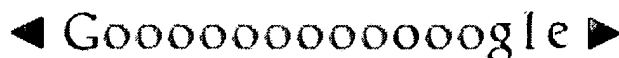
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for user ... emerging standards for Open **Distributed Processing** with extensions ...

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... several decades from small custom- designed digital hardware to large **distributed processing** systems ... Internal to each clip is a message **queue** to buffer messages ...

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... like processor speed, available memory, length of current run **queue**, percentage of ... load balancing is that there is a lot of **distributed processing** power that ...

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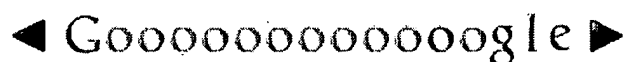
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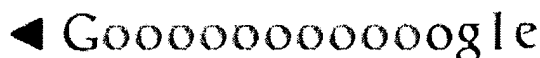
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